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JC32

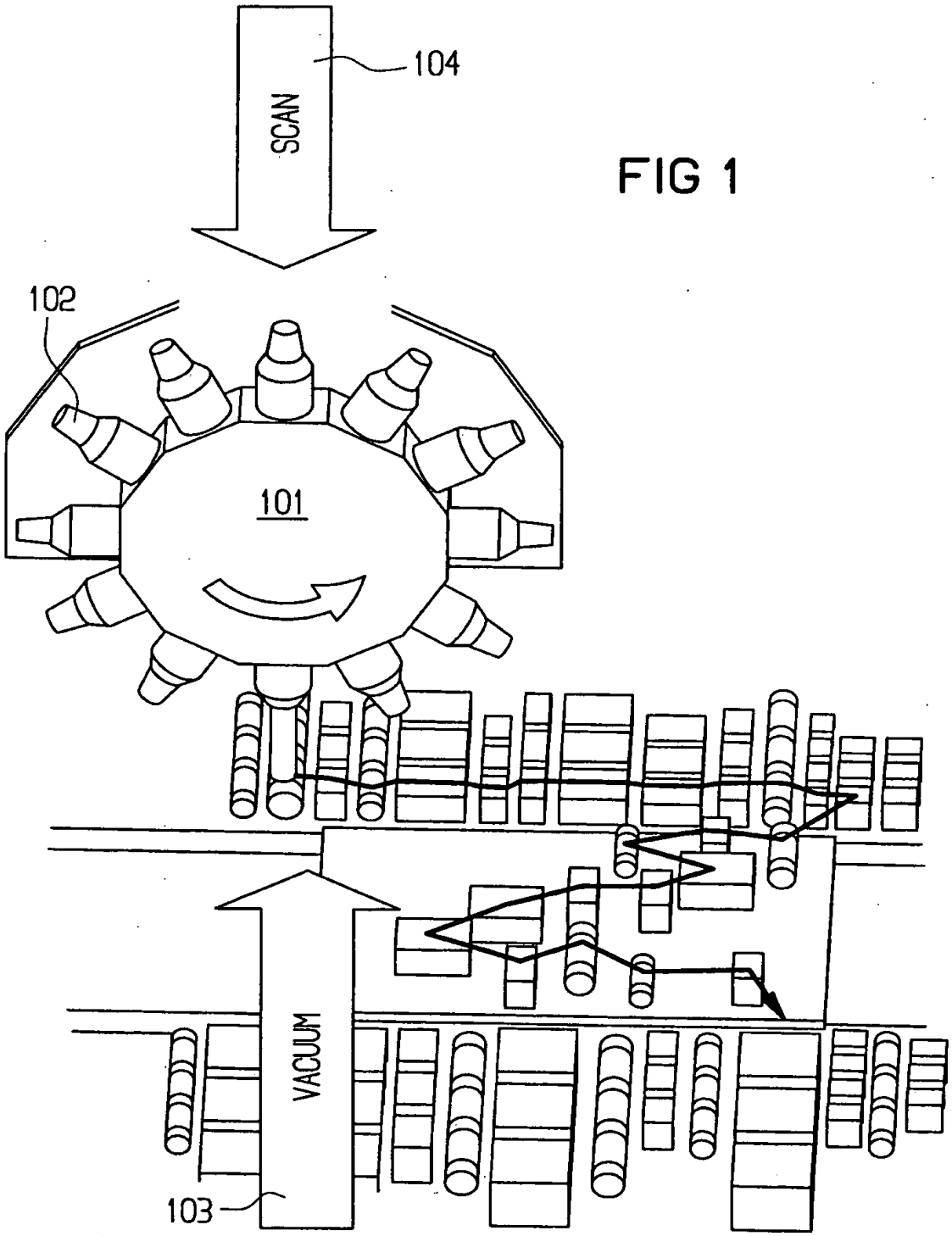

FIG 1

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FIG 2

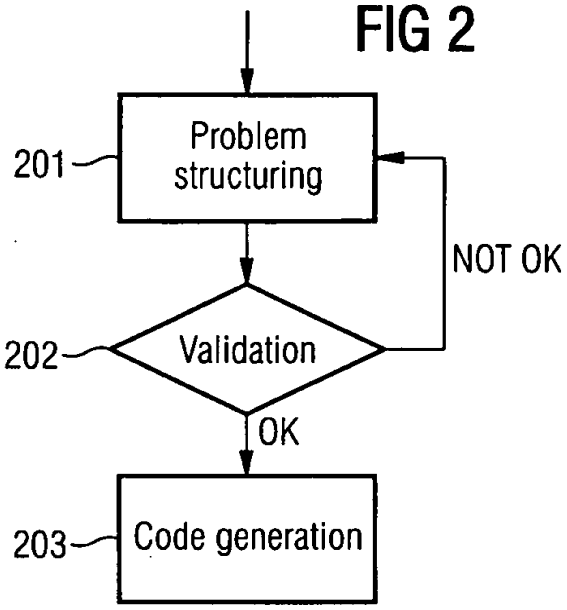
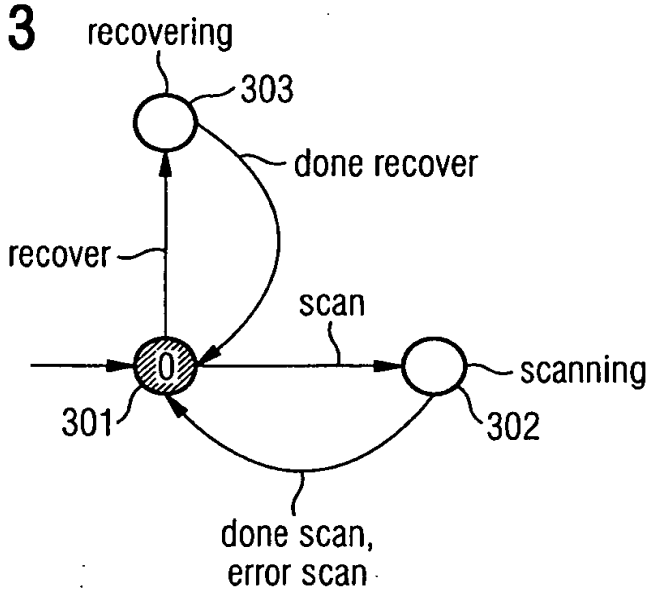


FIG 3



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SC32

FIG 4

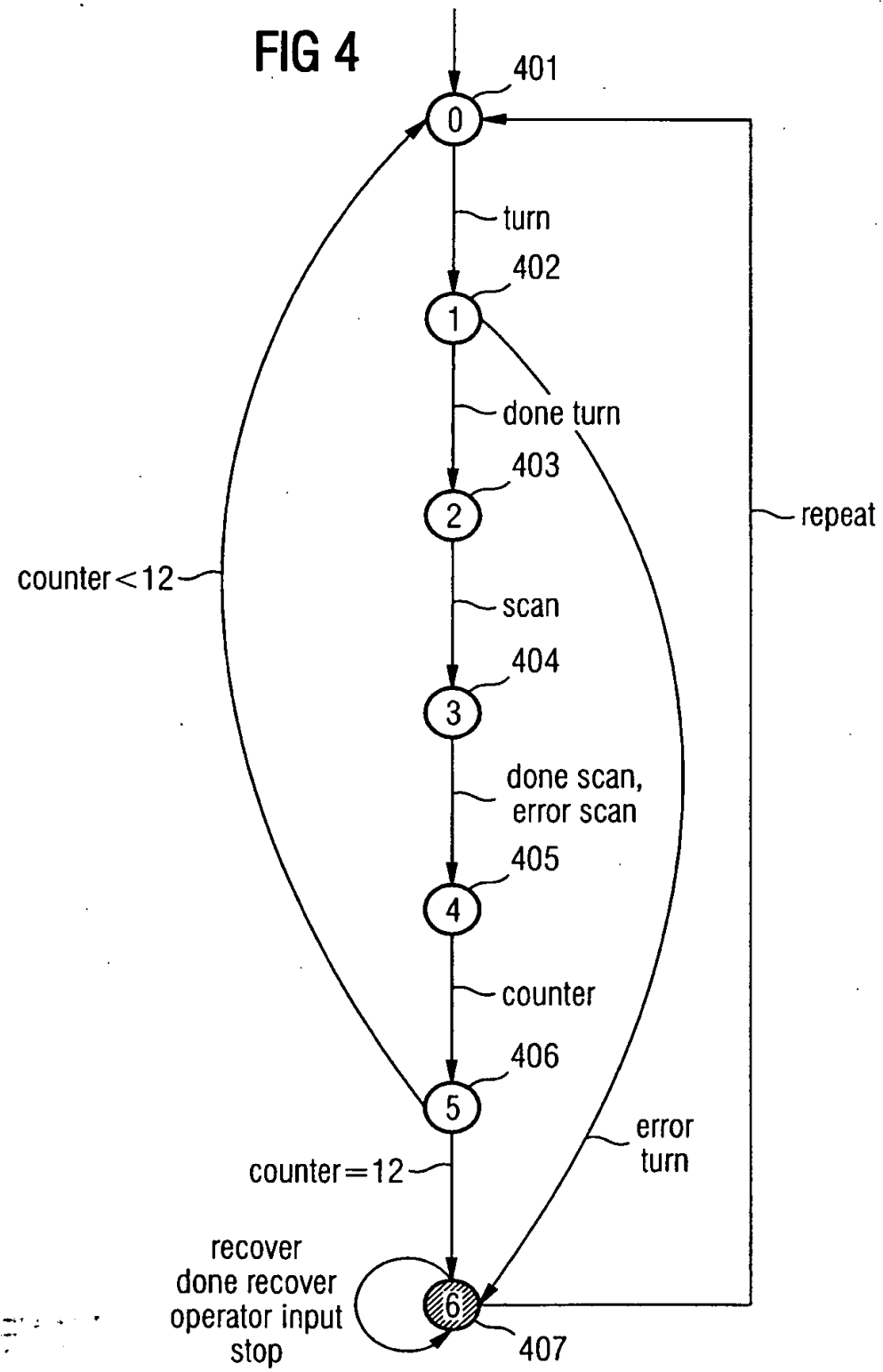

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FIG 5

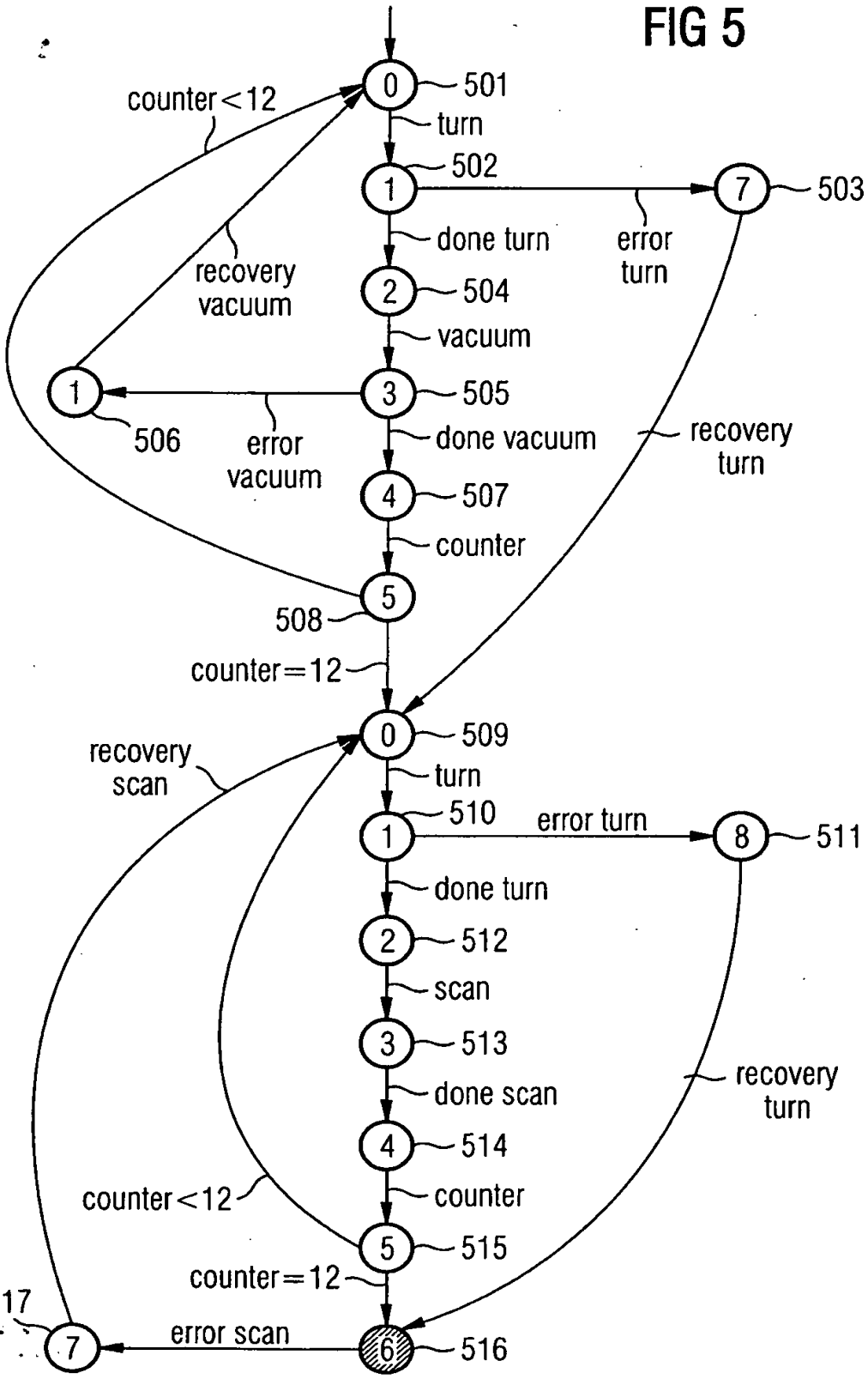

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FIG 6

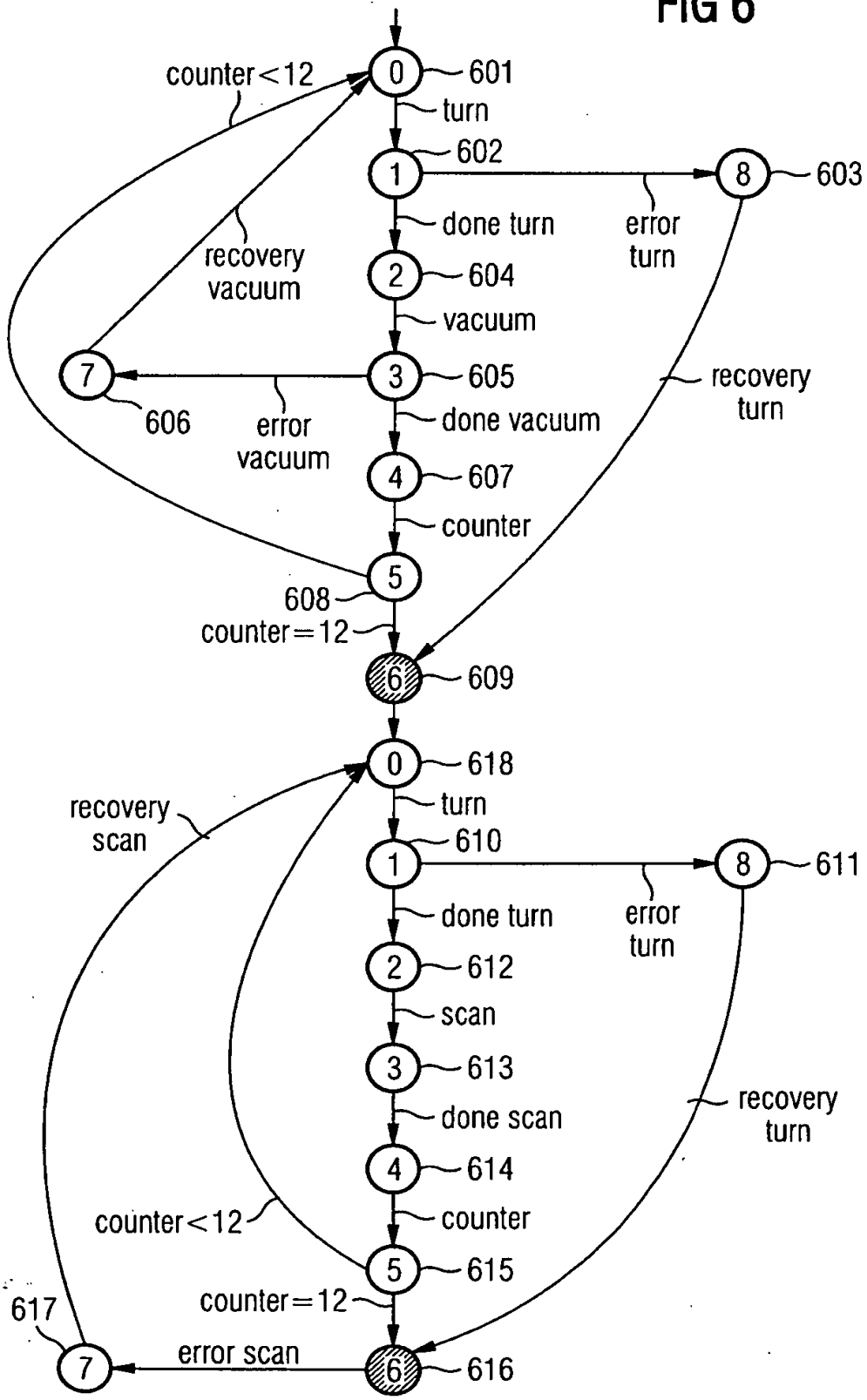

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FIG 7

System behaviour

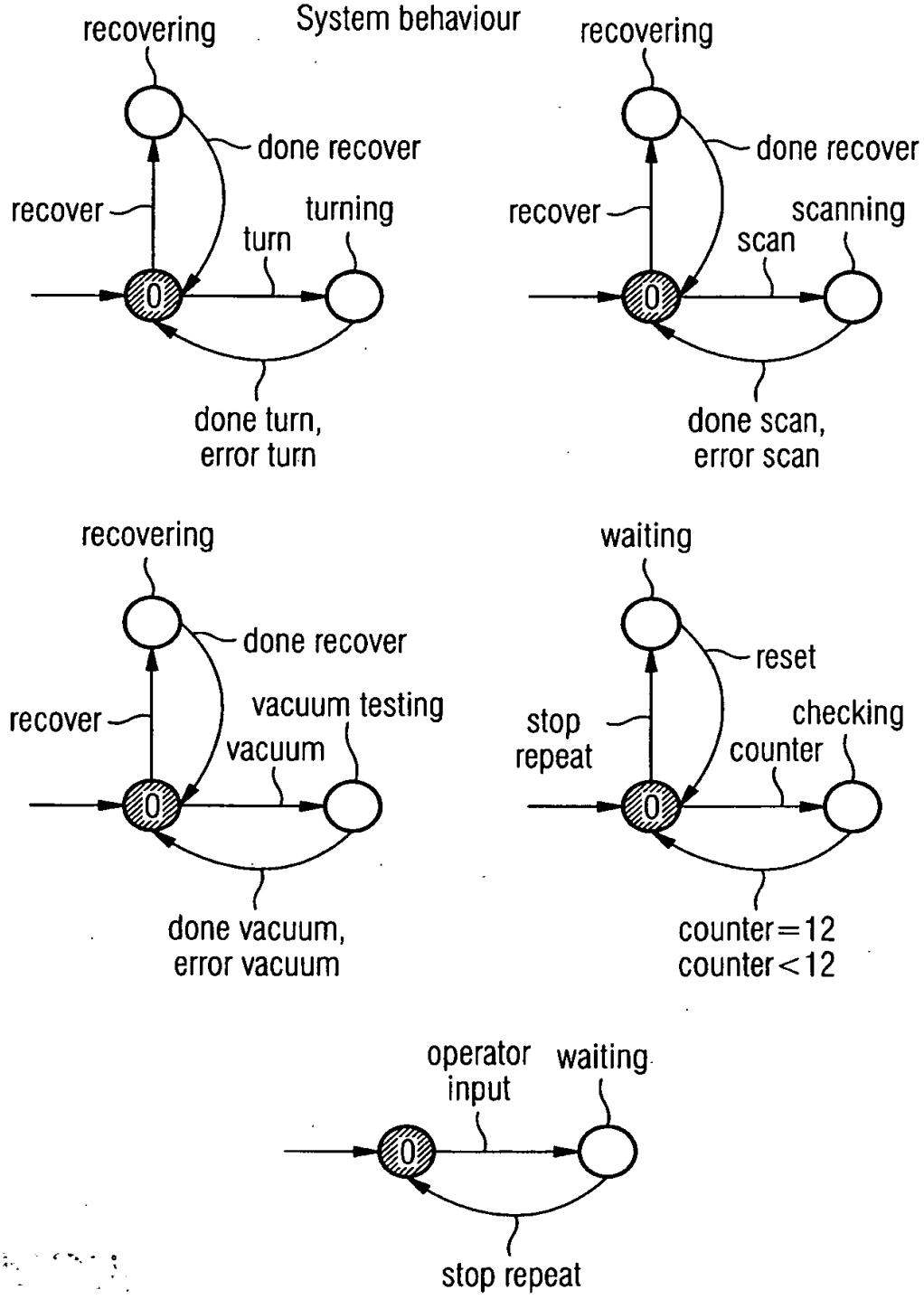

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FIG 8 Specified behaviour (error recovry)

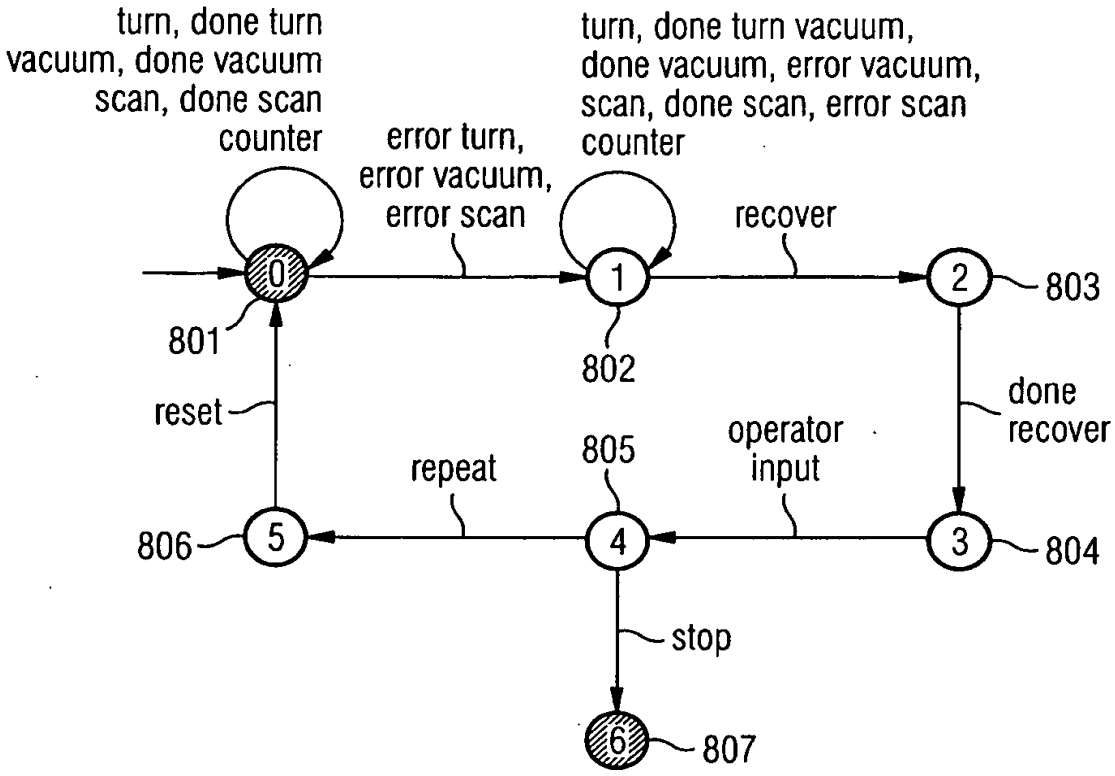

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FIG 9

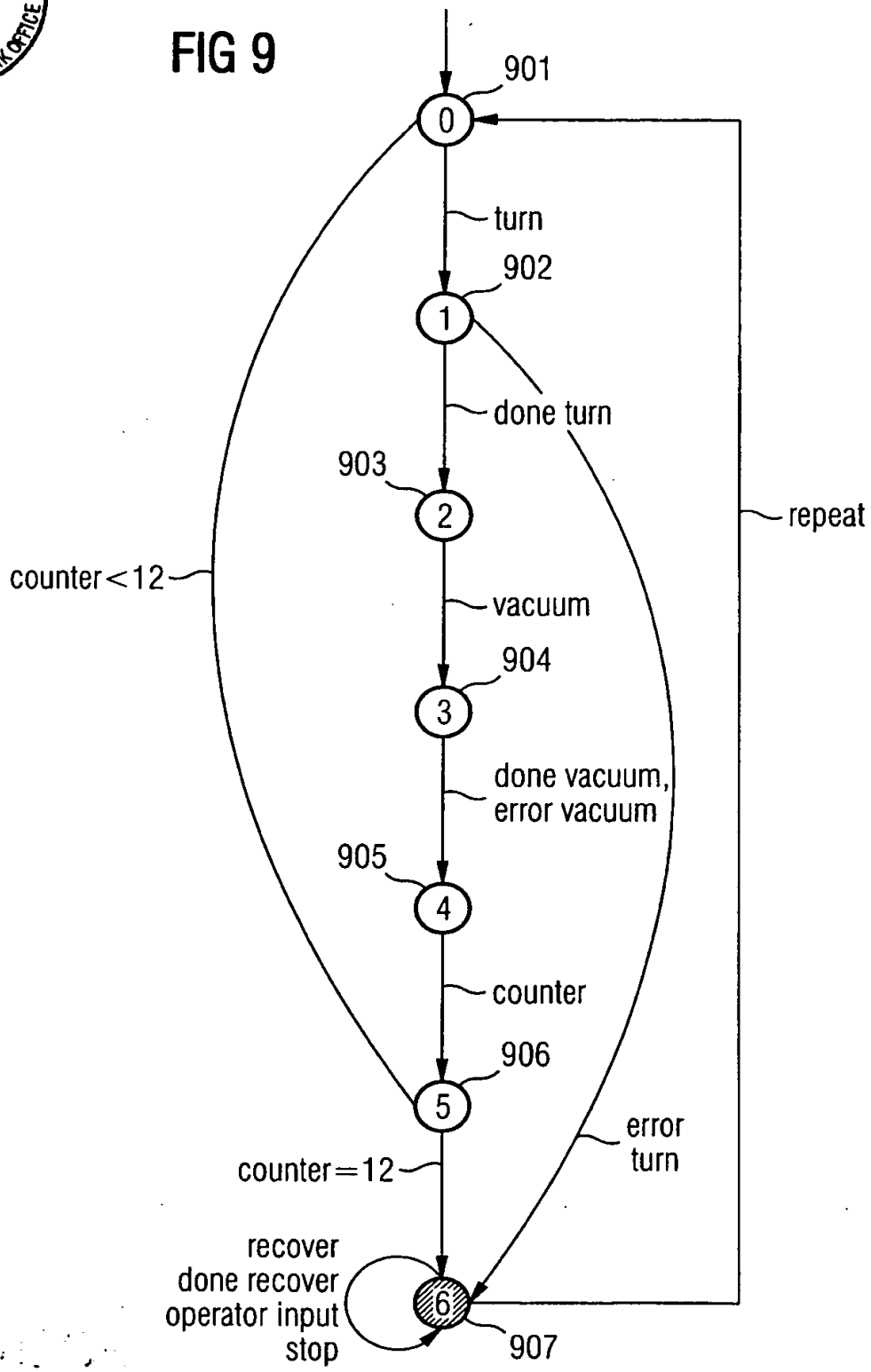

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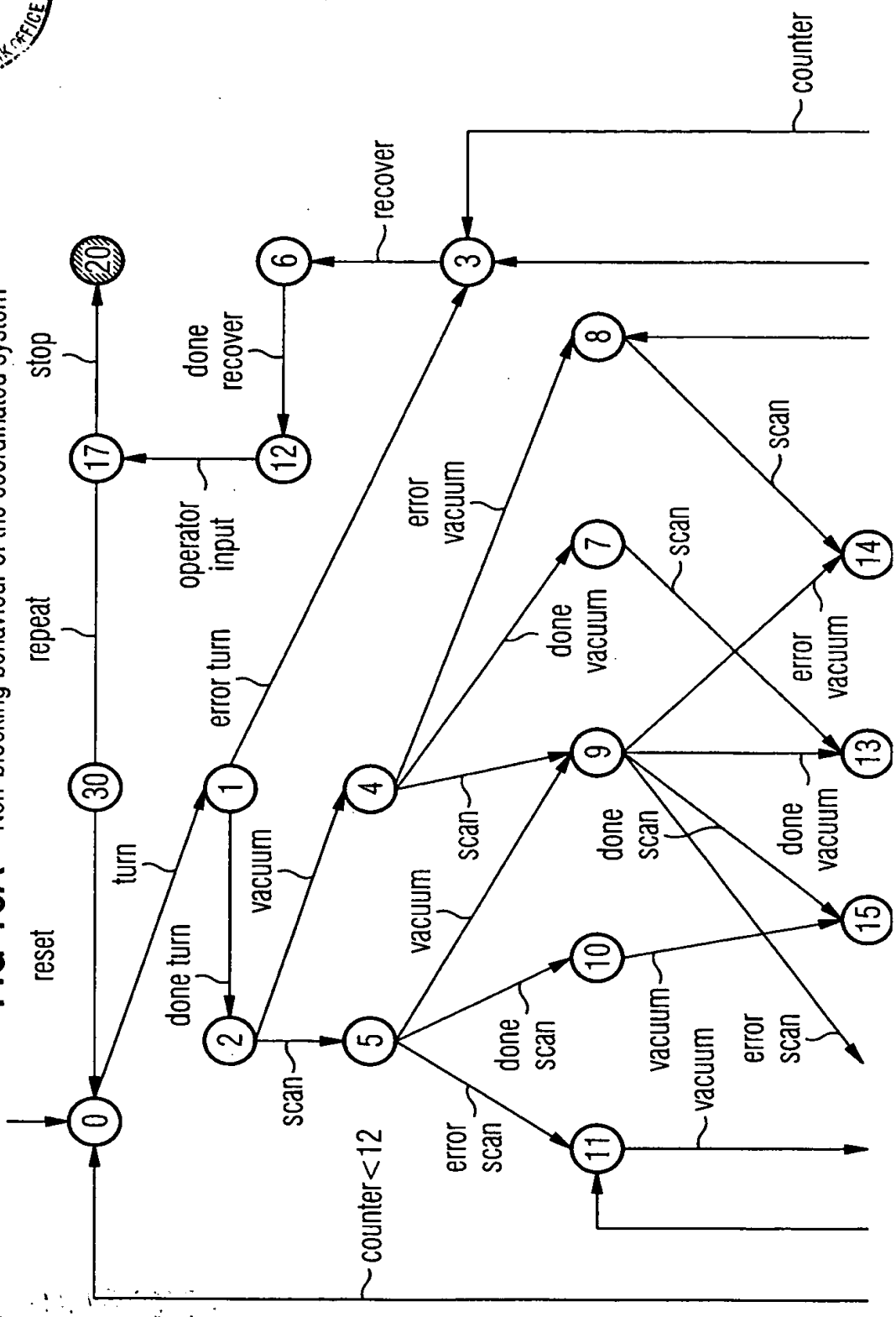
FIG 10A Non-blocking behaviour of the coordinated system



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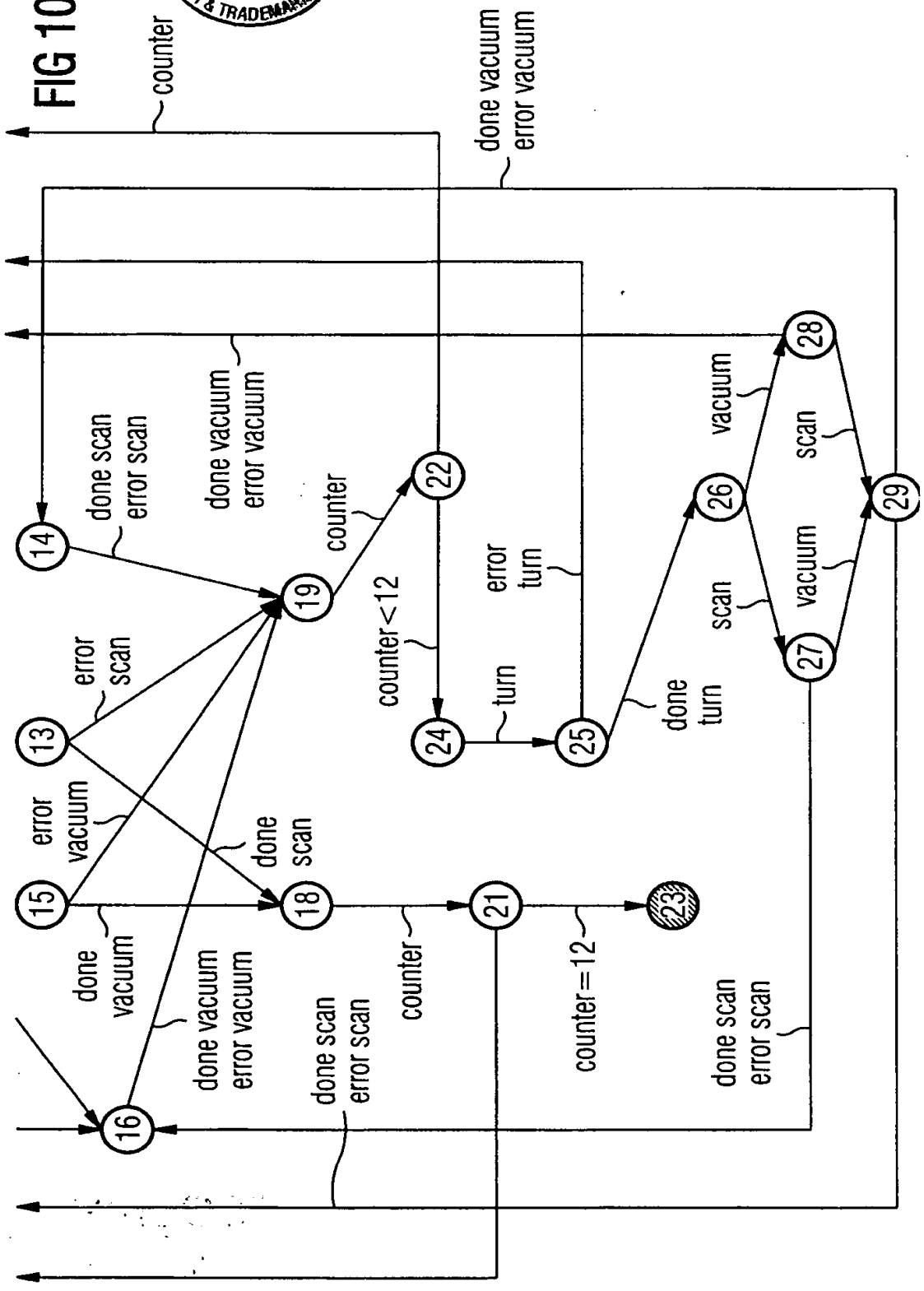
FIG 10 B